



Tim

Modified 02-03

PTO/SB/21 (01-03)
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application / Conf. No.	10/666,669 / 8241
	Filing Date	September 19, 2003
	First Named Inventor	Tim Tuan
	Examiner Name	Unknown
	Art Unit	2822
	Patent No.	
Mail Stop:		
Express Mail Receipt No.		
Total Number of Pages in This Submission		Attorney Docket Number X-1462 US

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Customer Number	24309 (Customer Number)	Reg. Number 51,959
Attn: Justin Liu		
Signature		
Date	July 30, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Tim Tuan et al.
Assignee: Xilinx, Inc.
Title: "Disabling Unused/Inactive Resources in
Programmable Logic Devices for Static Power
Reduction"

Serial No.: 10/666,669 File Date: 09/19/2003
Examiner: Unknown Art Unit: 2822
Docket No.: X-1462 US Conf. No.: 8241

COMMISSIONER FOR PATENTS
P.O. Box 1450
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the six (6) references listed in the attached Substitute for Form PTO-1449A/PT. One copy each of the four non-patent references is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu
Attorney for Applicants
Reg. No. 51,959

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Julie Matthews
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Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet

1

of

2

Complete if Known

Application / Conf. No.

10/666,669	/	8241
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Filing Date

September 19, 2003

First Named Inventor

Tim Tuan

Art Unit

2822

Examiner Name

Unknown

Attorney Docket Number

X-1462 US

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Date
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO		Complete if Known	
		Application / Conf. No.	10/666,669 / 8241
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	September 19, 2003
		First Named Inventor	Tim Tuan
		Art Unit	2822
		Examiner Name	Unknown
		Attorney Docket Number	X-1462 US
Sheet	2	of	2

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Jae Y. Park, Mark G. Allen; "A Comparison of Micromachined Inductors with Different Magnetic Core Materials"; 1996 Electronic Components and Technology Conference; 1996 IEEE; pp. 375-381.	
		Thomas D. Burd et al.; "A Dynamic Voltage Scaled Microprocessor System"; 2000 IEEE; IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, November 2000; pp. 1571-1580.	
		Anthony J. Stratakos et al.; "A Low-Voltage CMOS DC-DC Converter for a Portable Battery-Operated System"; 1994 IEEE; pp. 619-626.	
		Anthony John Stratakos; "High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications," Chapter 3; "DC-DC Converter Fundamentals"; pp. 42-78.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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